Docket No.: 4459-090A PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Po Jen CHENG et al.

Confirmation No. -----

U.S. Patent Application No. -----

Group Art Unit: 2829

Filed: February 10, 2004

Examiner: J. Hollington

For:

TEST ASSEMBLY, FOR INTEGRATED CIRCUIT PACKAGE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months after the U.S. filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required.

The references were cited by or submitted to the U.S. Patent and Trademark Office in *Parent Application No. 10/121,650*, filed April 15, 2002, which is relied upon for an earlier filing date under 35 USC 120. Thus copies of these references are not attached. 37 CFR 1.98 (d).

Respectfull submitted,

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Date: February 10, 2004

INFO		DISCLOSURE	ATTY. DOCKE 4459-090A	ATTY. DOCKET NO. 4459-090A		U.S. PATENT APPLICATION NO.		
	CITATIO	N IN AN	APPLICANT					
	APPLICA	ATION	Po Jen CHENO	G et al.				
(7000 4440)			FILING DATE		GROUP			
	(PTO-1	1449)	February 10, 2	004				
		U.S. PATENT	DOCUMENTS				i.	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.